

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-12 (canceled).

Claim 13 (Currently Amended): A circuit arrangement for aircraft engine controllers for providing or generating a bipolar direct current output signal as a function of at least one pulse-width modulated input signal, comprising: a first driver stage and a second driver stage, of the first and the second driver stages being activatable by a pulse-width modulated input signal, and at least one of the first and second driver stages being connected to a step-down converter stage so that, when the first driver stage is activated, a first switching element of the step-down converter stage activates a low-pass device of the step-down converter stage, and, when the second driver stage is activated, a second switching element of the step-down converter stage activates the low-pass device of the step-down converter stage.

Claim 14 (Previously Presented): The circuit arrangement as recited in claim 13, where each of the first and second driver stages is connected to the buck converter stage.

Claim 15 (Previously Presented): The circuit arrangement as recited in claim 13, wherein each of the first and second switching elements of the step-down converter stage includes at least one transistor.

Claim 16 (Previously Presented): The circuit arrangement as recited in claim 15, wherein a diode cooperates with the transistors of the first and second switching elements of the step-down converter stage.

Claim 17 (Previously Presented): The circuit arrangement as recited in claim 14, where the low-pass device of the step-down converter stage includes an inductor operating in pulsating operation.

Claim 18 (Previously Presented): The circuit arrangement as recited in claim 14, wherein, when a positive output direct current signal is to be provided by the circuit arrangement, the first driver stage is activated by the pulse-width modulated input signal, while a permanent high-level signal acts upon the second driver stage, and that the first switching element of the step-down converter stage subsequently activates the low-pass device of the step-down converter stage.

Claim 19 (Previously Presented): The circuit arrangement as recited in claim 14, wherein, when a negative direct current output signal is to be provided by the circuit arrangement, the second driver stage is activated by the pulse-width modulated input signal, while a permanent low-level signal acts upon the first driver stage, and that the second switching element of the step-down converter stage subsequently activates the low-pass device of the step-down converter stage.

Claim 20 (Previously Presented): The circuit arrangement as recited in claim 14, wherein the first switching element includes a PNP transistor and the second switching element includes an NPN transistor, a base of the PNP transistor of the first switching element is connected to the first driver stage and another base of the NPN transistor of the second switching element is connected to the second driver stage, a collector of the PNP transistor of the first switching element is connected to another collector of the NPN transistor of the second switching device, and an emitter of the PNP transistor of the first switching element being connected to a positive supply voltage terminal and another emitter of the NPN transistor of the second switching element being connected to a negative supply voltage terminal.

Claim 21 (Previously Presented): The circuit arrangement as recited in Claim 20, wherein a diode cooperates with the PNP transistor of the first switching element and is connected to the NPN transistor of the second switching element so that the cathode of one diode is connected to the other collector and the anode of the diode is connected to the other emitter of the NPN transistor.

Claim 22 (Previously Presented): The circuit arrangement as recited in Claim 20, wherein a diode cooperates with the NPN transistor of the second switching element and is connected to the PNP transistor of the first switching element so that the cathode of the diode is connected to the emitter and the anode of the diode is connected to the collector.

Claim 23 (Previously Presented): The circuit arrangement as recited in Claim 20, wherein a diode having a cathode cooperates with the PNP transistor of the first switching element and is connected to the collector of the PNP transistor and that the anode of the diode cooperates with the NPN transistor of the second switching element and is connected to the other collector.

Claim 24 (Previously Presented): The circuit arrangement as recited in Claim 20, wherein each of the first and second driver stages includes a transistor and resistors and capacitors connected to the transistor.